

Notic of References Cited	Application/Control No 10/043,458	Applicant(s)/Patent Under Reexamination TENG ET AL.	
	Examiner Stacy A Whitmore	Art Unit 2812	Page 1 of 1

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	V	Luis, J. et al., "Circuit synthesis of clock distribution networks based on non-zero clock skew", 1994, IEEE, pgs. 175 - 178.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.